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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Victor Chan, et al. Examiner: R.R. Forde
Serial No.: 10/674,644 Art Unit: 2826
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For: THREE DIMENSIONAL CMOS INTEGRATED CIRCUITS HAVING DEVICE LAYERS BUILT ON
DIFFERENT CRYSTAL ORIENTED WAFERS Dated: August 9, 2004

Confirmation No.: 4800

Commissioner for Patents
Alexandria, VA 22313-1450

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J. S. 8/23/04

AMENDMENT UNDER 37 C.F.R. § 1.312

Sir:

In connection with the above-identified patent application, kindly enter the following amendment to the specification which does not alter the scope of the allowed claims. This 312 Amendment is being filed prior to the payment of the issue fee.

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that this paper is being facsimile transmitted to the Patent and Trademark Office on the date shown below.

Dated: August 9, 2004


Leslie S. Szivos, Ph.D.